

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	369	(prefetch adj buffer\$2) decoder pipeline\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L2	154	(prefetch adj buffer\$2) decoder pipeline\$2 (ALU or (arithmetic adj unit))	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L3	143	(prefetch adj buffer\$2) decoder pipeline\$2 (ALU or (arithmetic adj unit)) control bus	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L4	139	(prefetch adj buffer\$2) decoder pipeline\$2 (ALU or (arithmetic adj unit)) control bus register	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L5	77	(prefetch adj buffer\$2) decoder pipeline\$2 (ALU or (arithmetic adj unit)) control bus register (conditional adj branch)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L6	73	(prefetch adj buffer\$2) decoder pipeline\$2 (ALU or (arithmetic adj unit)) control bus register (conditional adj branch) cache	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L7	73	L6 access	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L8	67	L7 (four near3 instruction\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L9	64	L8 (branch adj target)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L10	55	L9 (control adj signal\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L11	369	(prefetch adj buffer\$2) decoder pipeline\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25

EAST Search History

L12	154	(prefetch adj buffer\$2) decoder pipeline\$2 (ALU or (arithmetic adj unit))	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L13	143	(prefetch adj buffer\$2) decoder pipeline\$2 (ALU or (arithmetic adj unit)) control bus	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L14	139	(prefetch adj buffer\$2) decoder pipeline\$2 (ALU or (arithmetic adj unit)) control bus register	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L15	77	(prefetch adj buffer\$2) decoder pipeline\$2 (ALU or (arithmetic adj unit)) control bus register (conditional adj branch)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L16	73	(prefetch adj buffer\$2) decoder pipeline\$2 (ALU or (arithmetic adj unit)) control bus register (conditional adj branch) cache	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L17	73	L16 access	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L18	67	L17 (four near3 instruction\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L19	64	L18 (branch adj target)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L20	55	L19 (control adj signal\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L21	1	L20 (fetch near5 cancel)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L22	1	L20 (fetch near5 cancel\$5)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L23	53	L20 cancel\$5	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25

EAST Search History

L24	0	L20 (fetch near5 stop\$5)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L25	0	L20 (fetch near5 squash)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 11:25
L26	1	"6195735".pn.	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 13:56
L27	1	"4722050".pn.	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2007/03/06 13:56